Simulator Requirement Specification

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| --- | --- |
| Department Name | Electronics and Telecommunication |
| Class | BTech |
| Semester | VII |
| Subject Name | Power Electronics |
| Experiment No. | 1 |
| Experiment Name | Study V-I characteristics of SCR |

Version History

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Sr. No. | Version Number | Created By | Approved By | Date |
| 1 | v1.0 | Muskan Vantmori | Sonal lad | 10/10/2020 |
|  |  |  |  |  |

Detailed Requirement Specification

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| --- | --- | --- | --- |
| **Req. ID** | **Visual Entity Required** | **Requirement Description** | **Comments** |
| REQ1 | Vary input voltage | Vs | If any |
| REQ2 | Set gate voltage | VG |  |
| REQ2 | Anode to Cathode voltage | VAK | Voltmeter |
| REQ3 | Anode Current | IA | Ammeter |
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